

L Number	Hits	Search Text	DB	Time stamp
-	111	711/132.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 19:29
-	317891	stack	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 19:30
-	15738	stack with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:28
-	87	(stack with memory) and 711/132.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:29
-	781278	(access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:30
-	333	714/1.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:30
-	617	710/36.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:30
-	0	380/3.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:30
-	48	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:51
-	1	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (smart adj card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:52
-	6	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 20:55
-	2	("5507028").PN.) and (stack push pop heap load memory access\$3 control\$4 protect\$3 prevent\$3 smart card RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 21:08
-	48	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (stack push pop heap load memory access\$3 control\$4 protect\$3 prevent\$3 smart card RAM permit\$3 allow)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/31 21:09

	48	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (stack push pop heap load memory access\$3 control\$4 protect\$3 prevent\$3 smart card RAM permit\$3 allow\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:10
	48	((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (stack push pop heap load\$3 memory access\$3 control\$4 protect\$3 prevent\$3 smart card RAM permit\$3 allow\$3 function call)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:16
	1	stack near3 manipulat\$3 near3 attack\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:16
	113	stack near3 attack\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:46
	1	(stack near3 attack\$3) and (((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and ((stack with memory) and 711/132.ccls.) and (stack push pop heap load\$3 memory access\$3 control\$4 protect\$3 prevent\$3 smart card RAM permit\$3 allow\$3 function call))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:17
	9	(stack near3 attack\$3) and ((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3)) and (stack with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:33
	27	(stack near3 attack\$3) and (stack with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:33
	6654	((stack near3 attack\$3) (buffer near3 overflow\$3) (smash\$3 near3 stack))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:46
	156	((stack near3 attack\$3) (buffer near3 overflow\$3) (smash\$3 near3 stack)) and (stack with memory) and ((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 22:01
	2449	713/200.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:47
	11	((((stack near3 attack\$3) (buffer near3 overflow\$3) (smash\$3 near3 stack)) and (stack with memory) and ((access\$3 load\$3) with (control\$4 prevent\$3 restrict\$3))) and 713/200.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/31 21:47